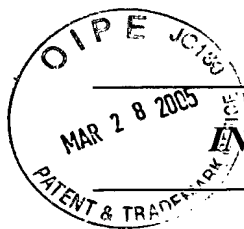


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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of: Fresko et al.

Attorney Docket No.: SUN1P803/P5258

Patent: 6,836,782 B1

Issued: December 28, 2004

Title: METHOD AND APPARATUS FOR
IMPLEMENTING MODULAR GARBAGE
COLLECTORS

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the U.S. Postal Service with sufficient postage as first-class mail on March 24, 2005 in an envelope addressed to the Commissioner for Patents, P.O. Box 1450 Alexandria, VA 22313-1450.

Signed: _____

Aurelia M. Sanchez

**REQUEST FOR CERTIFICATE OF CORRECTION
OF OFFICE MISTAKE
(35 U.S.C. §254, 37 CFR §1.322)**

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450
Attn: Certificate of Correction

Dear Sir:

Attached is Form PTO-1050 (Certificate of Correction) at least one copy of which is suitable for printing. The errors together with the exact page and line number where they occur, and shown correctly in the application filed, are as follows:

SPECIFICATION:

1. Column 7, line 19, change "traducer card" to --transducer card--. This appears correctly in the patent application as filed on April 17, 2001, on page 13, line 7.

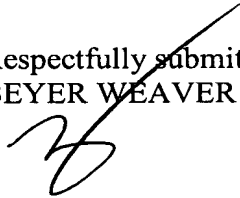
CLAIMS:

1. In line 1 of claim 10 (column 10, line 26) change "A computer" to --The computer--. This appears correctly in Amendment A as filed on July 26, 2004, on page 4, line 1.

Patentee hereby requests expedited issuance of the Certificate of Correction because the error lies with the Office and because the error is clearly disclosed in the records of the Office.. As required for expedited issuance, enclosed is documentation that unequivocally supports the patentee's assertion without needing reference to the patent file wrapper.

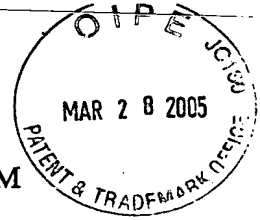
It is noted that the above-identified errors were printing errors that apparently occurred during the printing process. Accordingly, it is believed that no fees are due in connection with the filing of this Request for Certificate of Correction. However, if it is determined that any fees are due, the Commissioner is hereby authorized to charge such fees to Deposit Account 500388 (Order No. SUN1P803).

Respectfully submitted,
BEYER WEAVER & THOMAS, LLP



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1038, may, in appropriate cases, be incorporated in standard fashion as part of RAM 1036 as virtual memory. A specific primary storage device 1034 such as a CD-ROM may also pass data uni-directionally to the CPUs 1032.

5 CPUs 1032 are also coupled to one or more input/output devices 1040 that may include, but are not limited to, devices such as video monitors, track balls, mice, keyboards, microphones, touch-sensitive displays, transducer card readers, magnetic or paper tape readers, tablets, styluses, voice or handwriting recognizers, or other well-known input devices such as, of course, other computers. Finally, CPUs 1032
10 optionally may be coupled to a computer or telecommunications network, *e.g.*, an internet network or an intranet network, using a network connection as shown generally at 1012. With such a network connection, it is contemplated that the CPUs 1032 might receive information from the network, or might output information to the network in the course of performing the above-described method steps. Such
15 information, which is often represented as a sequence of instructions to be executed using CPUs 1032, may be received from and outputted to the network, for example, in the form of a computer data signal embodied in a carrier wave. The above-described devices and materials will be familiar to those of skill in the computer hardware and software arts.

20

As previously mentioned, a virtual machine may execute on computer system 1030. Figure 6 is a diagrammatic representation of a virtual machine which is supported by computer system 1030 of Figure 5, and is suitable for implementing the present invention. When a computer program, *e.g.*, a computer program written in the
25 Java™ programming language, is executed, source code 1110 is provided to a compiler 1120 within compile-time environment 1105. Compiler 1120 translates source code 1110 into bytecodes 1130. In general, source code 1110 is translated into bytecodes 1130 at the time source code 1110 is created by a software developer.

30 Bytecodes 1130 may generally be reproduced, downloaded, or otherwise distributed through a network, *e.g.*, network 1012 of Figure 5, or stored on a storage device such as primary storage 1034 of Figure 5. In the described embodiment,

Claim 10 (New): The computer-implemented interface of claim 9 wherein the second module further comprises a fifth routine for allocating an object in the virtual machine environment, and a sixth routine for performing the garbage collection process.

Claim 11 (New): The computer-implemented interface of claim 10 wherein the second module further includes at least a seventh routine for implementing barriers for the garbage collection process.

Claim 12 (New): The computer-implemented interface of claim 11 wherein the barriers include a read barrier and a write barrier.

Claim 13 (New): The computer-implemented interface of claim 7 wherein the first module includes a third routine for handling particular objects associated with the virtual machine environment.

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB Control number

(Also Form PT-1050)

UNITED STATES PATENT AND TRADEMARK OFFICE CERTIFICATE OF CORRECTION

PATENT NO. : 6,836,782 B1

DATED : December 28, 2004

INVENTOR(S) : Fresko et al.

It is certified that error appears in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In the Specifications:

Column 7, line 19, change "traducer card" to --transducer card--.

In the Claims:

In line 1 of claim 10 (column 10, line 26) change "A computer" to --The computer--.

MAILING ADDRESS OF SENDER:

PATENT NO. 6,836,782 B1

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